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PATENT  
1152-0263P

## IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: Hideaki SAKAGUCHI Conf.: 4601  
Appl. No.: 09/624,014 Group: 2857  
Filed: July 21, 2000 Examiner: Mary C. BARAN  
For: TESTING DEVICE & TESTING METHOD FOR  
SEMICONDUCTOR INTEGRATED CIRCUITS

**RESPONSE TO REQUEST FOR SUBSTITUTE SPECIFICATION**

OK to  
Enter  
MCB

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

October 9, 2003

Sir:

In reply to the Office Communication dated August 22, 2003, the period for response being extended one month to October 22, 2003, attached is a Substitute Specification in connection with the above-identified application, together with the marked-up copy thereof showing all additions in underlining and deletions in strike-through. No new matter has been introduced. Entry of the substitute specification is respectfully requested.

Pursuant to the provisions of 37 C.F.R. § 1.17 and § 1.136(a), Applicant hereby petitions for an extension of one (1) month in which to file a response to the outstanding Office Communication. The required fee of \$110.00 is attached hereto.

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Appl. No. 09/624,014

Should the Examiner have any questions concerning this application, the Examiner is invited to contact Robert W. Downs (Reg. No. 48,222) at (703) 205-8000.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. §§1.16 or 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By  #9211  
Terrell C. Birch, #19,382

<sup>RWD</sup>  
TCB/RWD/mzk

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